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Design a dedicated datapath for the digital computer system that can execute the algorithm in SNIPPET 1. Implement the datapath using only one adder (i.e. no adder-subtractor and no ALU) for all the arithmetic operations and also include the circuits for generating all of the status signals. The datapath is 4-bit wide.

Design a dedicated datapath for the digital computer ... CORDIC (COordinate Rotation Digital Computer) Computer

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Radix-2, binary number system (BNS) : irredundant, weighted,
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